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MAIL STOP PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

August 28, 2003

Re: Application of Koichi OHTO, Toshiyuki TAKEWAKI, Tatsuya USAMI,
Nobuyuki YAMANISHI
SEMICONDUCTOR DEVICE HAVING SILICON-DIFFUSED METAL
WIRING LAYER AND ITS MANUFACTURING METHOD
Assignee: NEC ELECTRONICS CORPORATION
Our Ref: Q77191

Dear Sir:

This is a request for a Continuation-in-Part Application of pending prior Application No. 10/281,321 (Confirmation No. 3881) filed October 28, 2002 of Koichi OHTO, Toshiyuki TAKEWAKI, Tatsuya USAMI, Nobuyuki YAMANISHI entitled SEMICONDUCTOR DEVICE HAVING SILICON-INCLUDING METAL WIRING LAYER AND ITS MANUFACTURING METHOD.

Attached hereto is the Continuation-in-Part Application comprising the specification, including the claims and abstract, 87 sheets of drawings, executed Declaration and Power of Attorney, executed Assignment and PTO 1595 form and a Preliminary Amendment.

The Government filing fee is calculated as follows:

Total claims	212	-	20	=	192	x	\$18.00	=	\$3456.00
Independent claims	17	-	3	=	14	x	\$84.00	=	\$1176.00
Base Fee									\$750.00

TOTAL FILING FEE	\$5382.00
Recordation of Assignment	\$ 40.00
TOTAL FEE	\$5422.00

Checks for the statutory filing fee of \$5382.00 and Assignment recordation fee of \$40.00 are attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this transmittal letter is attached.

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Priority is claimed from:

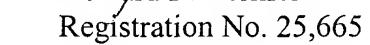
<u>Country</u>	<u>Application No</u>	<u>Filing Date</u>
Japan	2002-132780	May 8, 2002
Japan	2002-302841	October 17, 2002
Japan	2003-130484	May 5, 2003

The first two priority documents were filed in parent Application No. 10/281,321. The certified copy of JP-A 2003-130484 is enclosed herewith.

The application is timely filed.

Respectfully submitted,

SUGHRUE MION, PLLC
Attorneys for Applicant


By 
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